

Getting Started with Altera's DE1 Board

This document describes the scope of Altera's DE1 Development and Education Board and the supporting materials provided by the Altera Corporation. It also explains the installation process needed to use a DE1 board connected to a computer that has the Quartus[®] II CAD system installed on it.

Contents:

Purpose of the DE1 Board
Scope of the DE1 Board and Supporting Material
Installation and USB-Blaster Driver
Using the DE1 Board

Altera's DE1 Development and Education Board has been developed to provide an ideal vehicle for learning about digital logic and computer organization in a laboratory setting. It uses the state-of-the-art technology in both hardware and CAD tools to expose students to a wide range of topics covered in typical courses. The power of the board is such that it is also highly suitable for a variety of design projects as well as for the development of sophisticated digital systems. In addition to the DE1 board and the associated software, Altera provides supporting materials that include tutorials, laboratory exercises, and interesting demonstrations.

1 Purpose of the DE1 Board

University and college courses on the design of logic circuits and computer organization usually include a laboratory component. In a modern curriculum, the laboratory equipment should ideally exemplify state-of-the-art technology and design tools, but be suitable for exercises that range from the simple tasks that illustrate the most basic concepts to challenging designs that require knowledge of advanced topics. From the logistic point of view, it is ideal if the same equipment can be used in all cases. The DE1 board has been designed to provide the desired platform.

2 Scope of the DE1 Board and Supporting Material

The DE1 board features a powerful Cyclone[®] II FPGA chip. All important components on the board are connected to the pins of this chip, allowing the user to configure the connection between the various components as desired. For simple experiments, the DE1 board includes a sufficient number of switches (of both toggle and pushbutton variety), LEDs, and 7-segment displays. For more advanced experiments, there are SRAM, SDRAM, and Flash memory chips. For experiments that require a processor and simple I/O interfaces, it is easy to instantiate Altera's Nios II processor and use interface standards such as RS-232 and PS/2. For experiments that involve sound or video signals, there are standard connectors provided on the board. Finally, it is possible to connect other user-designed boards to the DE1 board by means of two expansion headers.

Software provided with the DE1 board features the Quartus II Web Edition design tools. It also includes a simple monitor program that allows the student to control various parts of the board in an easily understandable manner. There are also several applications that demonstrate the utility of the DE1 board.

Traditionally, manufacturers of educational FPGA boards have provided a variety of boards and the CAD tools needed to implement designs on these boards. However, there has been a paucity of supporting materials that could be used directly for teaching purposes. Altera's DE1 board is a significant departure from this trend. In addition to the DE1 board, Altera Corporation provides a full set of associated exercises that can be performed in a laboratory setting for typical courses on logic design and computer organization. In effect, the DE1 board and the available exercises can be used as a ready-to-teach platform for such laboratories. Of course, the DE1 board is also likely to be suitable for exercises that have been developed for other hardware platforms and can be ported to the DE1 platform.

3 Installation and USB-Blaster Driver

The DE1 board is shipped in a package that includes all parts necessary for its operation. The only essential parts are the 7.5-volt power adapter and the USB cable. There is also a protective plexiglass cover that may be used in the laboratory environment to protect the board from accidental physical damage.

Plug in the 7.5-volt adapter to provide power to the board. Use the USB cable to connect the USB connector on the DE1 board to a USB port on a computer that runs the Quartus II software. Turn on the power switch on the DE1 board.

The computer will recognize the new hardware connected to its USB port, but it will be unable to proceed if it does not have the required driver already installed. The DE1 board is programmed by using Altera's USB-Blaster mechanism. If the USB-Blaster driver is not already installed, the New Hardware Wizard in Figure 1 will appear.



Figure 1. Found New Hardware Wizard.

Since the desired driver is not available on the Windows Update Web site, select **No, not this time** in response to the question asked and click **Next**. This leads to the window in Figure 2.

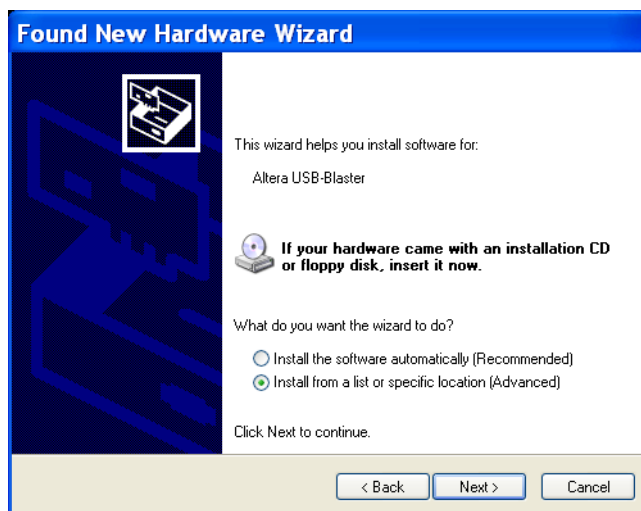


Figure 2. The driver is found in a specific location.

The driver is available within the Quartus II software. Hence, select **Install from a specific location** and click **Next** to get to Figure 3.

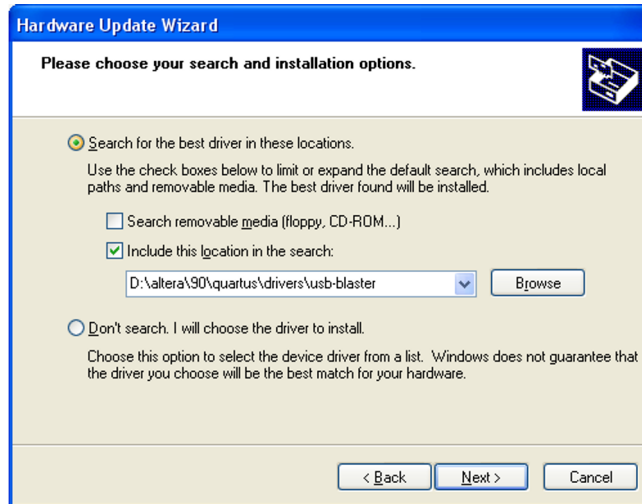


Figure 3. Specify the location of the driver.

Now, choose **Search for the best driver in these locations** and click **Browse** to get to the pop-up box in Figure 4. Find the desired driver, which is at location `altera\90\quartus\drivers\usb-blaster`. Click **OK** and then upon returning to Figure 3 click **Next**. At this point the installation will commence, but a dialog box in Figure 5 will appear indicating that the driver has not passed the Windows Logo testing. Click **Continue Anyway**.

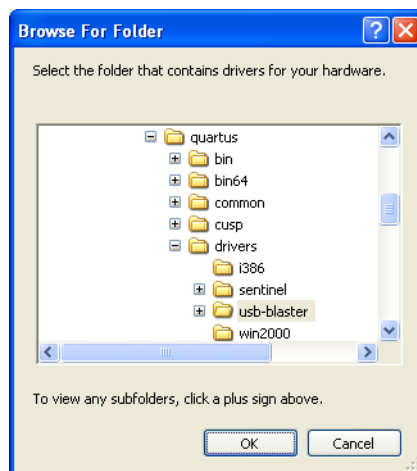


Figure 4. Browse to find the location.



Figure 5. There is no need to test the driver.

The driver will now be installed as indicated in Figure 6. Click Finish and you can start using the DE1 board.



Figure 6. The driver is installed.

4 Using the DE1 Board

The DE1 board is used in conjunction with the Quartus II software. A reader who is not familiar with this software should read an introductory tutorial. There are three versions of the tutorial:

- *Quartus II Introduction Using Verilog Design*
- *Quartus II Introduction Using VHDL Design*
- *Quartus II Introduction Using Schematic Design*

These tutorials cover the same aspects of the Quartus II software; they differ only in the design entry method that is used. They illustrate the entire process of implementing a design targeted for the DE1 board.

Copyright ©2009 Altera Corporation. All rights reserved. Altera, The Programmable Solutions Company, the stylized Altera logo, specific device designations, and all other words and logos that are identified as trademarks and/or service marks are, unless noted otherwise, the trademarks and service marks of Altera Corporation in the U.S. and other countries. All other product or service names are the property of their respective holders. Altera products are protected under numerous U.S. and foreign patents and pending applications, mask work rights, and copyrights. Altera warrants performance of its semiconductor products to current specifications in accordance with Altera's standard warranty, but reserves the right to make changes to any products and services at any time without notice. Altera assumes no responsibility or liability arising out of the application or use of any information, product, or service described herein except as expressly agreed to in writing by Altera Corporation. Altera customers are advised to obtain the latest version of device specifications before relying on any published information and before placing orders for products or services.

This document is being provided on an "as-is" basis and as an accommodation and therefore all warranties, representations or guarantees of any kind (whether express, implied or statutory) including, without limitation, warranties of merchantability, non-infringement, or fitness for a particular purpose, are specifically disclaimed.